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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/574,697	05/17/2000	Paul W. Chau	SPY-022-C1	8633

7590 01/26/2005

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EXAMINER	
NGUYEN, VAN H	
ART UNIT	PAPER NUMBER
2126	

DATE MAILED: 01/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/574,697	CHAU ET AL.	
	Examiner	Art Unit	
	VAN H NGUYEN	2126	

-- The MAILING DATE of this communication appears on th c ver sheet with the correspondence address --

Peri d for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *15 September 2004*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disp sition of Claims

4)☒ Claim(s) *1-9 and 41-77* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-9 and 41-77* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

1. Claims 1-9 and 41-77 are presented for examination.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-9 and 41-77 are rejected under 35 U.S.C. 102(e) as being anticipated by **Ohki et al.** (U.S. 6,016,957).

4. **As to claim 1**, Ohki teaches the invention as claimed including an integrated circuit card interface device *(e.g., an IC card reader/writer; see figs. 3 and 4)*, comprising:

- an application memory *(e.g., 1113; fig.4);*

- an application engine *(e.g., 1115; fig.4)* for managing one or more applications *(e.g., encryption, money switching; fig.4)* in said application memory;

- an input/output module *(e.g., 1112; fig.4)*;

- a host interface *(e.g., 1117; fig. 4)*;

- one or more integrated circuit card interfaces *(e.g., 111; fig. 4)*;

wherein the interface device is adapted to enable operation in accordance with multiple modes of operation *(e.g., (1) reading data from and writing data into the IC card 10...controlling basis operation of the IC card; col.9, line 67-col.10, line 4 and fig. 1 (2 exchanging data between the IC card reader/writer body and the personal computer; col.10, lines 17-17 and fig. 12)*;

5. As to claim 2, Ohki teaches a read-only memory *(e.g., ROM 1113; fig. 4).*

6. As to claim 3, Ohki teaches an electrically erasable programmable read-only memory *(e.g., EEPROM 105; fig.5).*

7. As to claim 4, Ohki teaches a microcontroller *(e.g., 1109; fig. 4).*

8. As to claim 5, Ohki teaches said microcontroller further comprises said application memory *(e.g., 1110; fig. 4).*

9. As to claim 6, Ohki teaches wherein said input/output module comprises a microcontroller *(e.g., 1108; fig. 4).*

10. As to claim 7, Ohki teaches a custom circuit *(col.8, line 63-col.9, line 5).*

11. As to claim 8, Ohki teaches said custom circuit further comprises said application memory *(col.9, lines 48-51).*

12. As to claim 9, Ohki teaches said input/output module further comprises a custom circuit *(see fig. 11).*

13. As to claim 41, Ohki teaches the interface device is portable *(see figs. 3&4).*

14. As to claim 42, Ohki teaches each mode of operation is characterized by the nature of the device or devices, if any, to which the interface device con be operably connected to enable communication therebetween during operation in that mode *(col.10, lines 33-63).*

15. As to claim 43, Ohki teaches a standalone mode of operation in which the interface device is not operably connected to a host device via the host interface *(col.9, lines 66-col.10, line 1).*

16. As to claim 44, Ohki teaches a mode of operation in which the interface device is operably connected *(e.g., 1105; fig. 4)* to an integrated circuit card *(e.g., 10; fig. 4)* via one of the one or more integrated circuit card interfaces *(e.g., 111; fig. 4)* to enable communication between the interface device and the integrated circuit card *(fig. 4).*

17. As to claim 45, Ohki teaches a mode of operation in which the interface device is not operably connected to another device to enable communication therebetween *(col.12, lines 26-38 and fig. 7).*

18. As to claim 46, Ohki teaches a connected mode of operation in which the interface device *(e.g., IC card reader; fig. 4)* is operably connected to a host device *(e.g., the personal computer; fig. 12)* via the host interface *(e.g., 1117; fig. 4)* to enable communication between the interface device and the host device *(col.10, lines 15-17).*

19. As to claim 47, Ohki teaches during the connected mode of operation the interface device *(e.g., IC card reader; fig. 4)* is also operably connected *(e.g., 1105; fig. 4)* to an integrated circuit card *(e.g., 10; fig. 4)* via one of the one or more integrated circuit card interfaces *(e.g., 111; fig. 4)* to enable communication between the interface device and the integrated circuit card *(col.9, line 66-col.10, line 1).*

20. As to claim 48, Ohki teaches a connected mode of operation in which in interface device *(e.g., IC card reader; fig. 4)* is operably connected to a host device *(e.g., the personal computer; fig. 12)* via the host interface *(e.g., 1117; fig. 12)* to enable communication between the interface device and the host device *(col.10, lines 15-17)*.

21. As to claim 49, it includes the same limitations as in claim 47 above, and is similarly rejected under the same rationale.

22. As to claims 50-53, they include the same limitations as in claims 45-48 above, and are similarly rejected under the same rationale.

23. As to claims 54-55, they include the same limitations as in claims 47-48 above, and are similarly rejected under the same rationale.

24. As to claim 56, it includes the same limitations as in claim 47 above, and is similarly rejected under the same rationale.

25. As to claim 57, Ohki teaches a programming mode of operation in which the interface device *(e.g., IC card reader; fig. 4)* is operably connected *(e.g., 1105; fig. 4)* to an integrated circuit card *(e.g., 10; fig. 4)* via one of the one or more integrated circuit card interfaces *(e.g., 111; fig. 4)*, and/or to a host device via the host interface, to enable one or more programs to be added to and/or deleted from, the interface device *(col.10, lines 27-32)*.

26. As to claim 58, Ohki teaches the interface device *(e.g., IC card reader; fig. 4)* is operably connected *(e.g., 1105; fig. 4)* to an integrated circuit card *(e.g., 10; fig. 4)* via one of the one or more integrated circuit card interfaces *(e.g., 111; fig. 4)* to enable one or more programs to be added to, and/or deleted from *(e.g., 1104; fig. 4)*, the interface device during the programming mode of operation.

27. As to claim 59, Ohki teaches the interface device *(e.g., IC card reader/writer; col.13, lines 66-67)* is operably connected to *(e.g., connected to; col.13, lines 66-67)* a host device *(e.g., a personal computer; col.13, lines 66-67and fig. 12)* via the host interface *(e.g., 1117; fig. 4)* to enable one or more programs to be added to, and/or deleted from, the interface device during the programming mode of operation *(col.13, line 66-col.14, lines 39).*

28. As to claim 60, Ohki teaches the interface device *(e.g., IC card reader/writer; fig. 4)* is operably connected to *(e.g., 1105; fig. 4)* an integrated circuit card *(e.g., 10; fig. 4)* via one of the one or more integrated circuit card interfaces *(e.g., 111; fig. 4)* and to a host device *(e.g., a personal computer; col.13, lines 66-67and fig. 12)* via the host interface *(e.g., 1117; fig. 4)* to enable one or more programs to be added to, and/or deleted from, the interface device during the programming mode of operation *(col.13, line 66-col.14, lines 39)*

29. As to claims 61-66, they include the same limitations as in claims 43-48 above, and are similarly rejected under the same rationale.

30. As to claim 67, it includes the same limitations as in claim 47 above, and is similarly rejected under the same rationale.

31. As to claim 68, Ohki teaches a display unit *(e.g., 115; fig.3)* and an input unit *(e.g., 113; fig.3).*

32. **As to claim 69**, Ohki teaches the invention as claimed including a portable integrated circuit card interface device *(e.g., an IC card reader/writer; see fig. 4)* comprising:

means for operably connecting *(e.g., 1105; fig.4)* the interface device *(e.g., an IC card reader/writer; fig. 4)* to an integrated circuit card *(e.g., IC card 10; fig.4)* to enable communication between the interface device and the integrated circuit card;

means for operably connecting *(e.g., 1117; fig.4)* the interface device to a host device *(e.g., the personal computer; col.10, line 17 and lines 56-63)* to enable communication between the interface device and the host device;

means for operating the interface device in a standalone mode in which the interface device is not operably connected to a host device to enable communication between the interface device and the host device *(e.g., a reader/writer circuit 1108 for reading data from and writing data into the IC card; col. 9, line 66- col.10, line 9 and col.10, lines 33-55)*; and

means for operating the interface device in a connected mode in which the interface device is operably connected to a host device to enable communication between the interface device and the host device *(e.g., an interface circuit 1117...for exchanging data between the IC card and reader/writer body and the personal computer; col.10, lines 15-17 and 56-63).*

33. As to claim 70, Ohki teaches a node *(e.g., 1105; fig. 4)* in which the interface device *(e.g., an IC card reader/writer; fig. 4)* is operably connected to an integrated circuit card *(e.g.,10; fig. 4)* to enable communication between the interface device and the integrated circuit card *(col.9, line 66-col.10, line 1).*

16. As to claim 71, Ohki teaches a mode in which the interface device is not operably connected to another device to enable communication therebetween *(col.12, lines 26-38 and fig. 7).*

34. As to claim 72, Ohki teaches the connected mode comprises operably connecting *(e.g., 1105; fig.4)* the interface device *(e.g., IC reader; fig.4)* to an integrated circuit card *(e.g., IC card 10; fig.4)* to enable communication between the interface device and the integrated circuit card *(col.9, line 66-col.10, line 1).*

35. As to claims 73 and 74, they include the same limitations as in claim 72 above, and are similarly rejected under the same rationale.

36. As to claim 75, it includes the same limitations as in claim 71 above, and is similarly rejected under the same rationale.

37. As to claim 76, it includes the same limitations as in claim 72 above, and is similarly rejected under the same rationale.

38. As to claim 77, it includes the same limitations as in claim 57 above, and is similarly rejected under the same rationale.

Response to Arguments

39. Applicant's arguments with respect to claims 1-9 and 41-77 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

40. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

41. A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

42. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VAN H. NGUYEN whose telephone number is (571) 272-3765. The examiner can normally be reached on Monday-Thursday from 8:30AM - 6:00PM. The examiner can also be reached on alternative Friday.

43. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor Meng-Ai An can be reached on (571) 272-3756.

44. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

45. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:
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